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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/286,168 04/05/1999 PAT 6,404,007

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
Verified and Acknowledged				

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TITLE

Method of forming a trench transistor having a superior gate dielectric

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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